

**Faculty of Technology  
University of Delhi**

**Tentative Mid - Semester Examination Datesheet (Session 2024 - 2025): Even Semester**

<b>S. No.</b>	<b>Date</b>	<b>Category</b>	<b>Time</b>	<b>Subject</b>	<b>Semester</b>
1	3/3/2025	DSC - 4	02 - 03 PM	Mathematics - II	II
2	4/3/2025	DSC - 2	02 - 03 PM	Physics	II
3	4/3/2025	DSC - 5	02 - 03 PM	Introduction to Electrical and Electronics Engineering	II
4	5/3/2025	DSC - 6	02 - 03 PM	Data Structures (CSE-A, CSE-B, ECE-B, EE)	II
<b>S. No.</b>	<b>Date</b>	<b>Category</b>	<b>Time</b>	<b>Subject</b>	<b>Semester</b>
1	3/3/2025	DSC - 10	11 AM - 12 PM	Operating System	IV
2	3/3/2025	DSC - 10	11 AM - 12 PM	Signals and Systems	IV
3	3/3/2025	DSC - 10	11 AM - 12 PM	Electrical Machines II	IV
4	4/3/2025	DSC - 11	11 AM - 12 PM	Software Engineering	IV
5	4/3/2025	DSC - 11	11 AM - 12 PM	Electromagnetic Theory (EMT)	IV
6	4/3/2025	DSC - 11	11 AM - 12 PM	Power Transmission and Distribution	IV
7	5/3/2025	DSC - 12	11 AM - 12 PM	Computer System Architecture	IV
8	5/3/2025	DSC - 12	11 AM - 12 PM	Linear Integrated Circuits	IV
9	5/3/2025	DSC - 12	11 AM - 12 PM	Electrical and Electronic Measurements	IV
10	6/3/2025	DSE - 2/ GE - 4	11 AM - 12 PM	Foundations of Data Analysis (G1)	IV
11	6/3/2025	DSE - 2/ GE - 4	11 AM - 12 PM	Microelectronics Design	IV
12	6/3/2025	DSE - 2/ GE - 4	11 AM - 12 PM	Electric Vehicle Motor	IV

**Dr. Vanita Jain  
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